



JRW

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant:** PRAKASH NARAIN et al.

**Serial No.:** 10/717,386

**Filed:** November 18, 2003

**For:** INTENT-DRIVEN FUNCTIONAL VERIFICATION  
OF DIGITAL DESIGNS

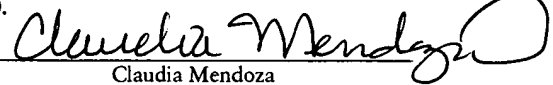
**Examiner:** Not yet known

**Group Art Unit:** 2825

**Docket No.** 59165-298553

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. 1.97(B)(3) – W/O FEE AND 1.98**

Dear Sir:

Applicant hereby submits this Supplemental Information Disclosure Statement which is being filed before the mailing date of a first Office Action on the merits.

Examiner is requested to consider the references noted on the enclosed Form PTO/SB/08b during examination of the above-identified patent application. These references are submitted for the Examiner's consideration and are submitted pursuant to the duty of disclosure under 37 C.F.R. § 1.56. In submitting these references, no representation is made or implied that the references are or are not material to the examination of the application. The Examiner is encouraged to make his or her own determination of materiality. In accordance with 37 C.F.R. § 1.98(a)(2)(i), copies of the U.S. references are not provided herewith.

**INVITATION FOR A TELEPHONE INTERVIEW**

If the Examiner has any questions concerning the relevance of any reference cited in this disclosure, the Examiner is respectfully requested to call the undersigned at (303) 607-3633.

**CHARGE OUR DEPOSIT ACCOUNT**

Pursuant to 37 C.F.R. § 1.97(b)(3), no fees are due with respect to this filing. However, should any fees be deemed necessary, such fees may be charged to Deposit Account No. 06-0029.

Respectfully submitted,

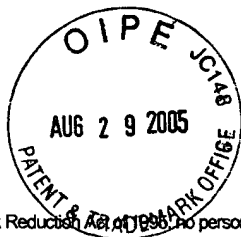
FAEGRE & BENSON, LLP

CUSTOMER No.: 35657

Date: August 25, 2005

By: 

Michael A. DeSanctis  
Atty. Reg. No.: 39,957  
Telephone: 303.607.3633



PTO/SB/08b (05-03)

Approved for use through 04/30/2003. OMB 0651-0031

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 1

**Complete if Known**

Application Number	10/717,386
Filing Date	November 18, 2003
First Named Inventor	Prakash Narain et al.
Art Unit	2825
Examiner Name	Not yet known
Attorney Docket Number	59165-298553

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		SFORZA et al., "A 'Design for Verification' Methodology." March 2001. Quality Electronic Design. 2001 International Symposium. pp 50-55.	
		OMNES et al., "Using SSDE for USB2.0 Conformance Co-Verification." June 2003. Formal Methods and Models for Co-Design, 2003. MEMOCODE '03. Proceedings. First ACM and IEEE International Conference. pp. 113-122.	
		BHASKAR et al., "A Universal Random Test Generator for Functional Verification of Microprocessors and System-On-Chip." January 2005. VLSI Design 2005. 18 <sup>th</sup> International Conference. pp. 207-212.	

Examiner Signature		Date Considered	
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citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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